

Improving performance of opto-electronic reservoir computers with online learning

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Reservoir Computing is a bio-inspired computing paradigm for processing time dependent signals. Its performance on a series of benchmark tasks is equal and sometimes better than other digital algorithms. It is particularly well suited for analog implementations, but these are often constrained by the offline training algorithms commonly employed. Here we present an opto-electronic reservoir computer trained online by an FPGA chip. We test the system on a channel equalisation task and report error rates up to two orders of magnitude lower than previous implementations. Moreover, we demonstrate how online training allows the computer to adapt to a non-stationary task.

Introduction

Reservoir Computing (RC) is a set of methods for designing and training artificial recurrent neural networks [1]. Although it greatly simplifies the training process, the resulting performances are equal to other algorithms on a series of benchmark tasks [2]. This paradigm is well suited for analog implementations: several opto-electronic [3–5] and all-optical reservoir computers [6–8] have been reported.

The performance of a reservoir computer greatly relies on the training technique. Up to now experimental reservoir computers have been trained using offline learning methods [3, 4, 6–10]. An alternative approach is to use online training in which the readout weights are progressively adapted in real time [11]. We have investigated an example of such algorithms in simulations [12] and experimentally [13], with performance close to previous offline-trained implementations. These works are based on the same opto-electronic reservoir as in [3], but trained online by an FPGA board.

In this work we present important improvements over [12, 13]. Further experimental investigations allow us to significantly increase the performance on the nonlinear channel equalisation task [14]. We also consider a simple case of a variable communication channel and show that online training can deal with non-stationary problems, whereas it would be difficult to adapt offline training methods to such tasks.

Experimental Reservoir Computing

A general reservoir computer is described in [2]. In our implementation, depicted in figure 1(a), we use a sine function $f = \sin(x)$, as in [3, 4], and a ring topology [15] to simplify the interconnection matrix, so that only the first neighbour nodes are connected.

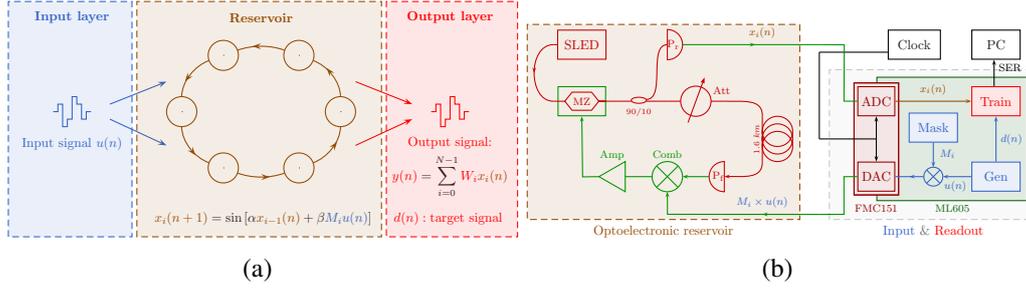


Figure 1: **(a)** Schematic representation of our reservoir computer. It contains $N = 50$ internal variables $x_i(n)$ evolving in discrete time $n \in \mathbb{Z}$, $\sin(x)$ is the nonlinear function, $u(n)$ is an external signal that is injected into the system, α and β parameters are used to adjust the feedback and the input signals, respectively, and M_i is the input mask, drawn from a uniform distribution over the interval $[-1, +1]$. The readout weights w_i are trained online by minimising the error between the output signal $y(n)$ and the target signal $d(n)$. **(b)** Schematic representation of the experimental setup.

The reservoir computer is trained online using the simple gradient descent algorithm [16], with the following rule for updating the readout weights:

$$w_i(n+1) = w_i(n) + \lambda(n) (d(n) - y(n)) x_i(n), \quad (1)$$

where $\lambda(n)$ is the step size, used to control the learning rate. It starts at $\lambda(0) = 0.4$, and then gradually decreases to zero following $\lambda(n+1) = \gamma\lambda(n)$, with $\gamma < 1$.

Figure 1(b) depicts our experimental setup containing the opto-electronic reservoir and the FPGA board. A personal computer is used to control the experiment, scan the parameters α , β and γ and collect the results.

The opto-electronic reservoir is a replica of [3]. The reservoir states are encoded into the intensity of the incoherent light signal, produced by a superluminescent diode (SLED). The Mach Zehnder intensity modulator (MZ) implements the sine function. The optical attenuator (Att) is used to set the feedback gain α of the system (see figure 1(a)). The fibre spool gives a round trip time of $7.94 \mu\text{s}$. The resistive combiner (Comb) sums the electrical feedback signal with the input signal from the FPGA.

The input and readout layers are programmed on a Xilinx ML605 board, powered by a Virtex 6 FPGA chip. The board is paired with a 4DSP FMC151 daughter card, containing two analog-to-digital converters (ADCs) and two digital-to-analog converters (DACs). For precise synchronisation of the FPGA with the experiment, it is driven by an external clock, generated by a Hewlett Packard 8648A signal generator. The design uses two Galois linear feedback shift registers with a total period of about 10^9 to generate pseudo-random symbols $d(n)$. These are used to compute the inputs $u(n)$ to the reservoir (see below). The inputs $u(n)$ are multiplied by the input mask M_i and sent to the opto-electronic reservoir through the DAC. The resulting reservoir states are collected and digitised by the ADC, and used to compute readout weights W_i and the output signal $y(n)$. The latter is compared to the input signal $d(n)$ and the resulting SER is transmitted to the computer.

Results

In wireless communications, the properties of a channel are highly influenced by the environment. For better equalisation performance, it is crucial to be able to detect significant

channel variations and adjust the RC readout weights in real time. The increasing demand for higher bandwidths in wireless communications will probably require fast and efficient analogue equalisers.

The channel is modelled by the following equations [1, 14]:

$$\begin{aligned}
 q(n) &= 0.08d(n+2) - 0.120d(n+1) + d(n) + 0.18d(n-1) \\
 &\quad - 0.10d(n-2) + 0.091d(n-3) - 0.05d(n-4) + 0.04d(n-5) \\
 &\quad + 0.03d(n-6) + 0.010d(n-7), \\
 u(n) &= p_1q(n) + 0.036q^2(n) - 0.011q^3(n) + v(n),
 \end{aligned}
 \tag{2}$$

where $v(n)$ is the noise. The reservoir computer has to recover the clean input signal $d(n) \in \{-3, -1, 1, 3\}$ from the noisy distorted output signal $u(n)$. The performance is measured in terms of wrongly reconstructed symbols, given by the Symbol Error Rate (SER). The $p_1 \in [0, 1]$ parameter is used to tweak the nonlinearity of the channel. In addition to a constant channel (with $p_1 = 1$), we consider here a simple example of a “switching” channel by changing regularly p_1 between three predefined values $p_1 \in \{0.60, 0.80, 1.00\}$.

Figure 2(a) presents the performance of our reservoir computer, evaluated over one million input symbols, for different signal-to-noise ratios of the wireless channel (green squares). Each value is an average of the lowest rates obtained with different random input masks, and the error bars show the variations of the performance with these masks. We compare our results to those reported in [3], obtained with the same opto-electronic reservoir, trained offline (blue dots). To the best of our knowledge, a SER of 5.71×10^{-6} at 32 dB SNR is the lowest error rate ever reported with an experimental reservoir computer on this task.

Figure 2(b) shows the error rate produced by our experiment in case of a switching noiseless communication channel. Every switch is followed by a steep increase of the SER, as the reservoir computer is no longer optimised for the channel it is equalising. The performance degradation is detected by the algorithm, causing the learning rate λ to be reset to the initial value λ_0 . The readout weights are then retrained to new optimal values, and the SER rate correspondingly goes down. We thus demonstrated that online-trained experimental reservoir computers are well suited for dealing with problems that change in real time.

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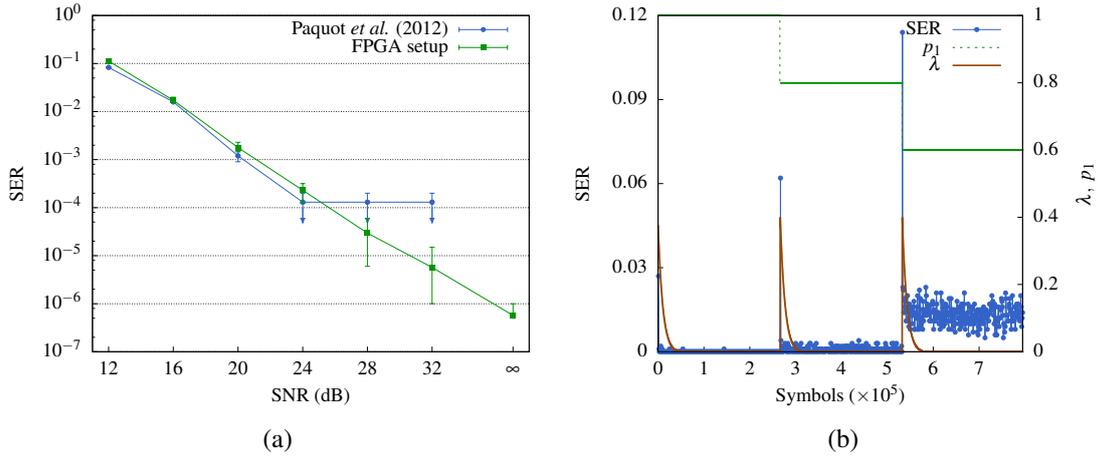


Figure 2: **(a)** Comparison of experimental results for nonlinear channel equalisation. **(b)** Experimental results for switching channel task. After each switch, the SER goes up, the learning rate λ is reset to λ_0 , and then decreases while the SER gradually goes back to a low value.

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